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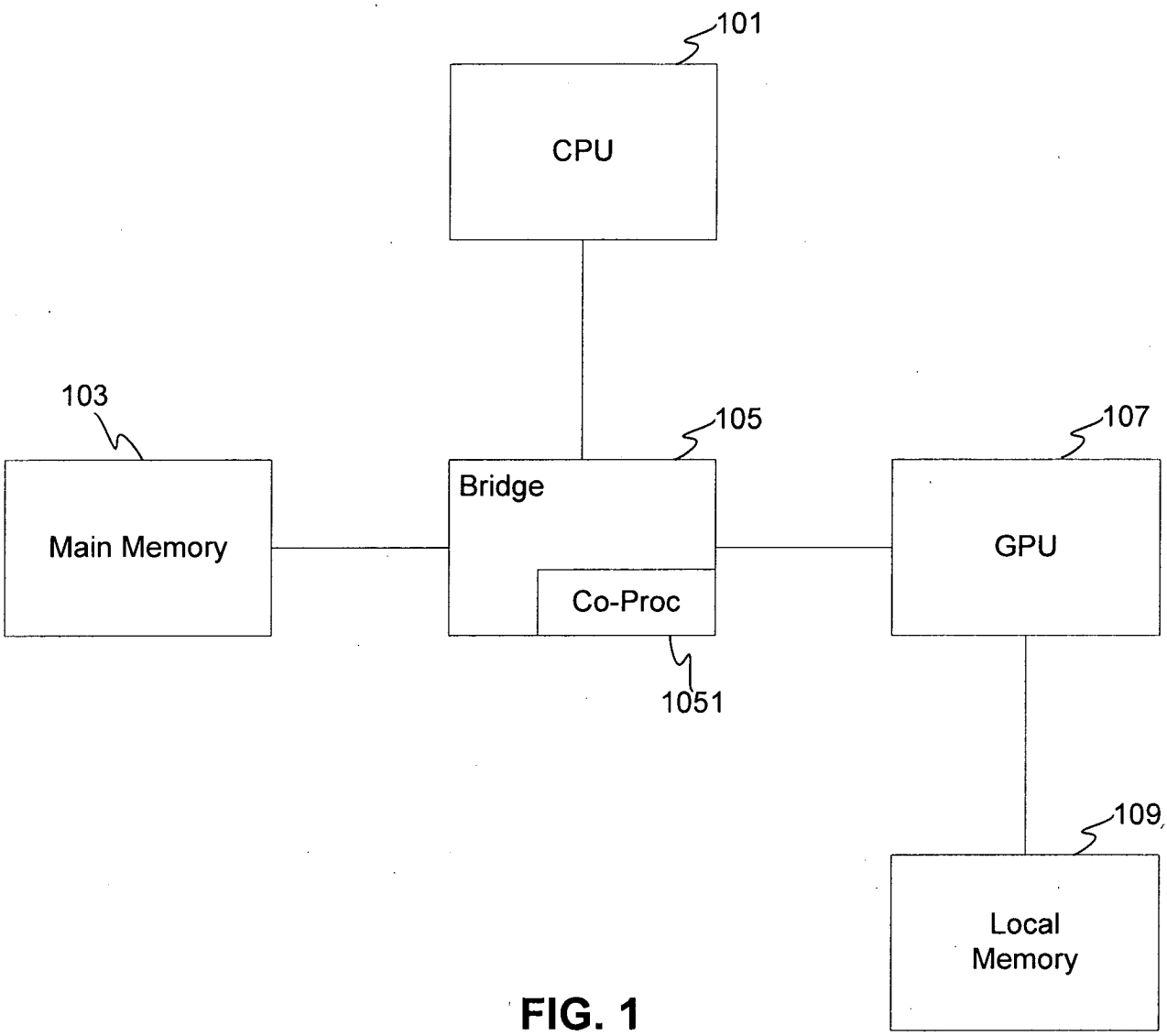


**FIG. 1**
**Prior Art**

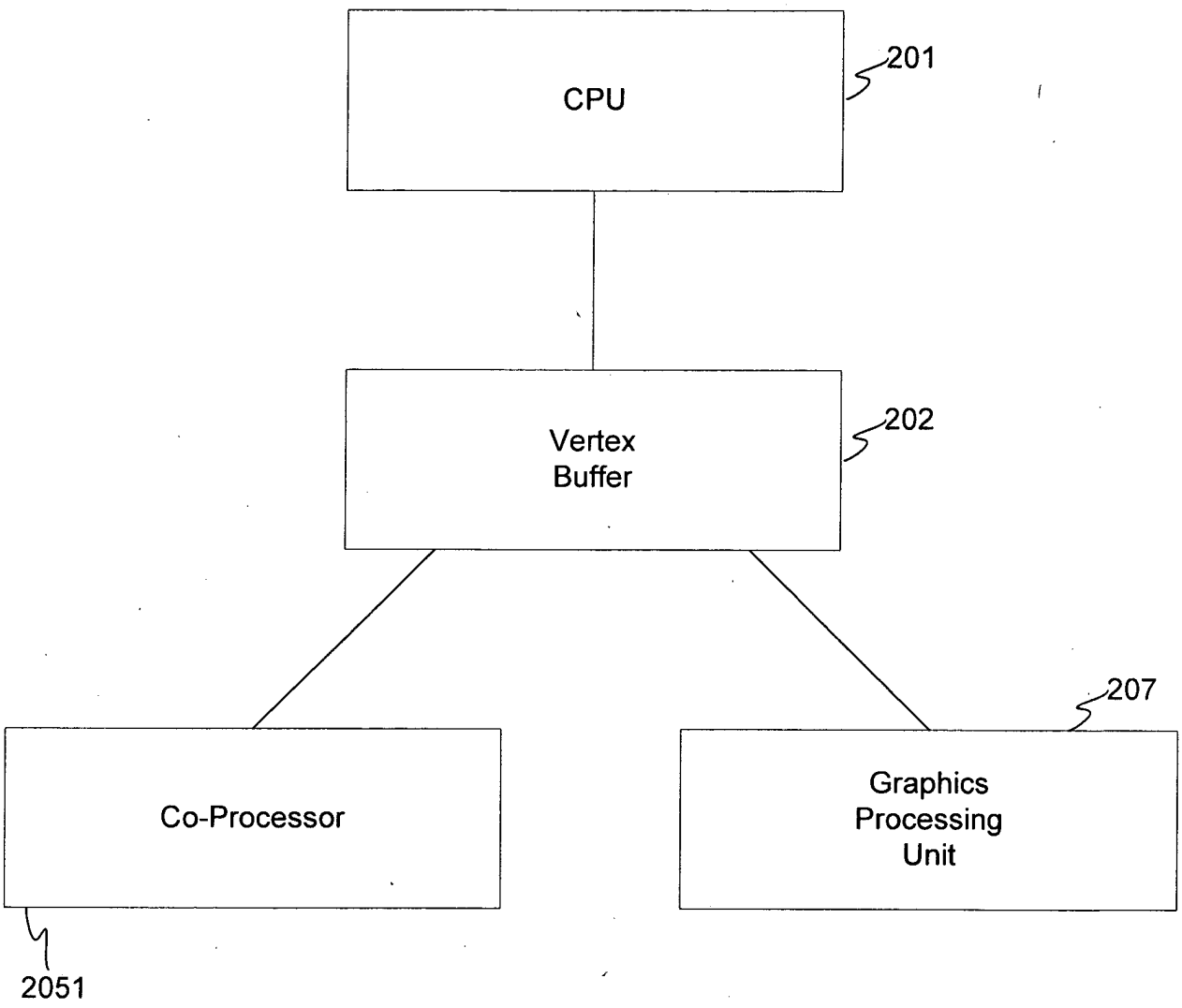


**FIG. 2**
**Prior Art**

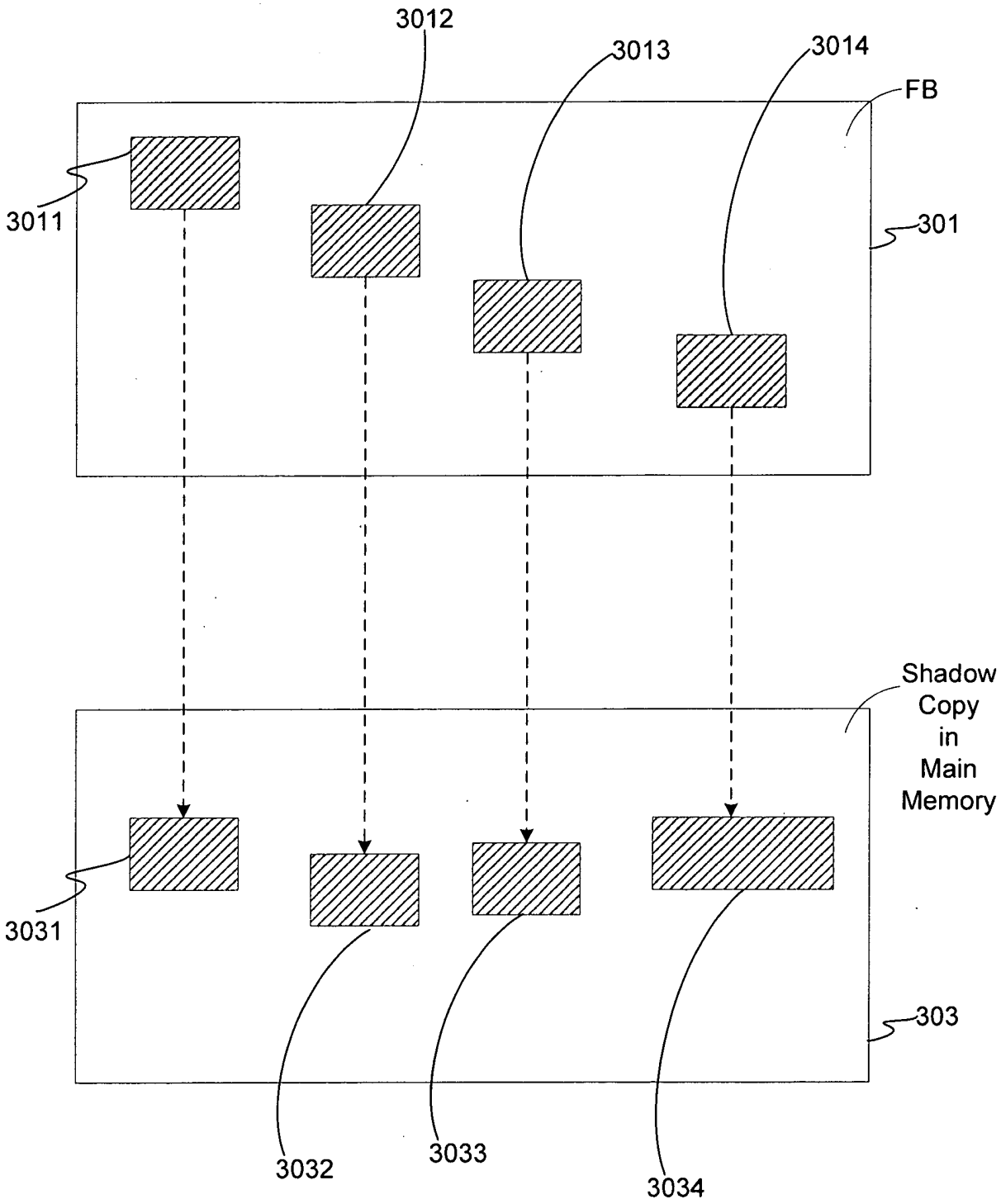
3012
3013
3014
FB
3011
301
Shadow
Copy
in
Main
Memory
3031
303
3032
3033
3034

FIG. 3

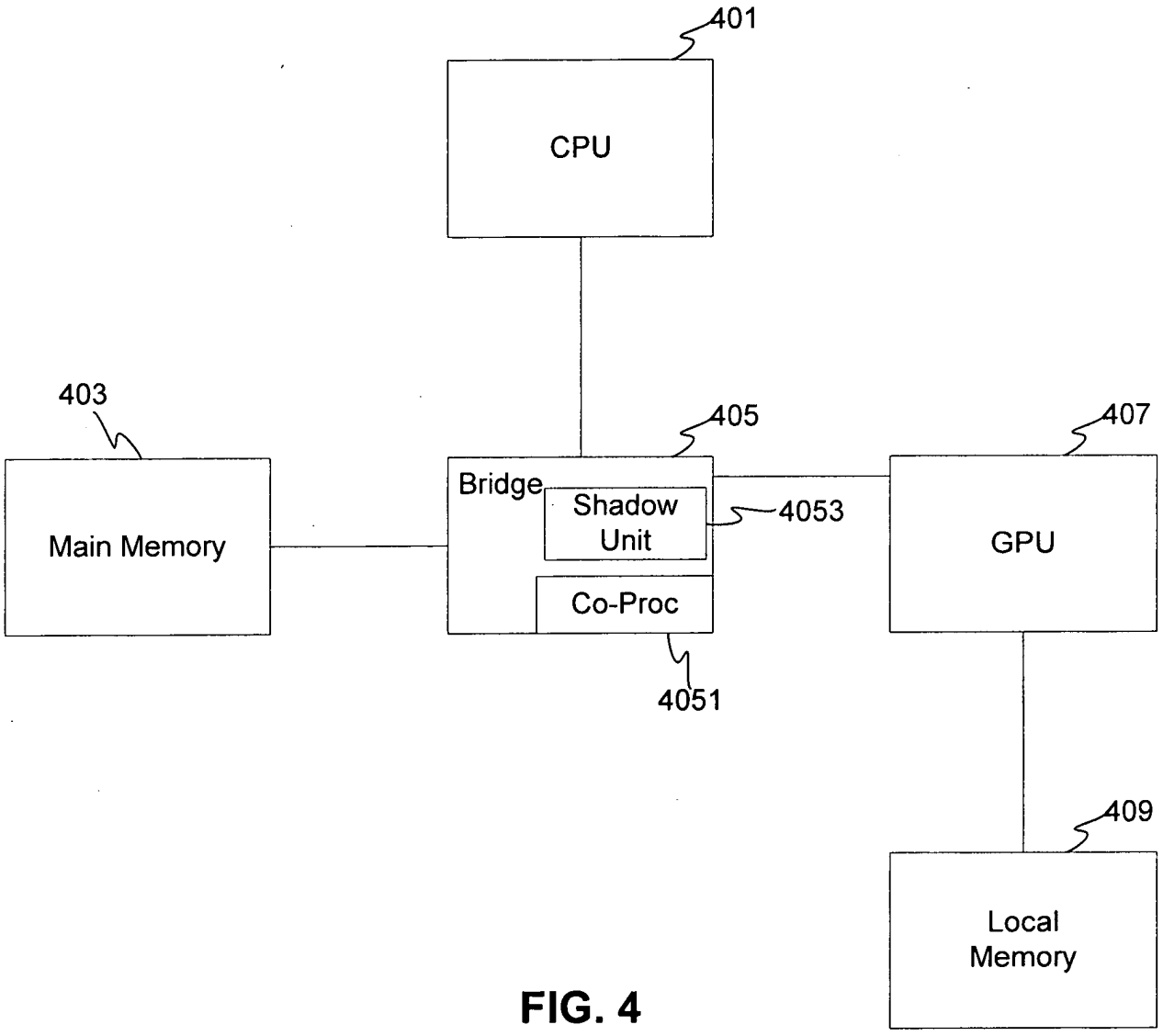
401
CPU
403
Main Memory
405
Bridge
Shadow Unit
4053
Co-Proc
4051
407
GPU
409
Local Memory

FIG. 4

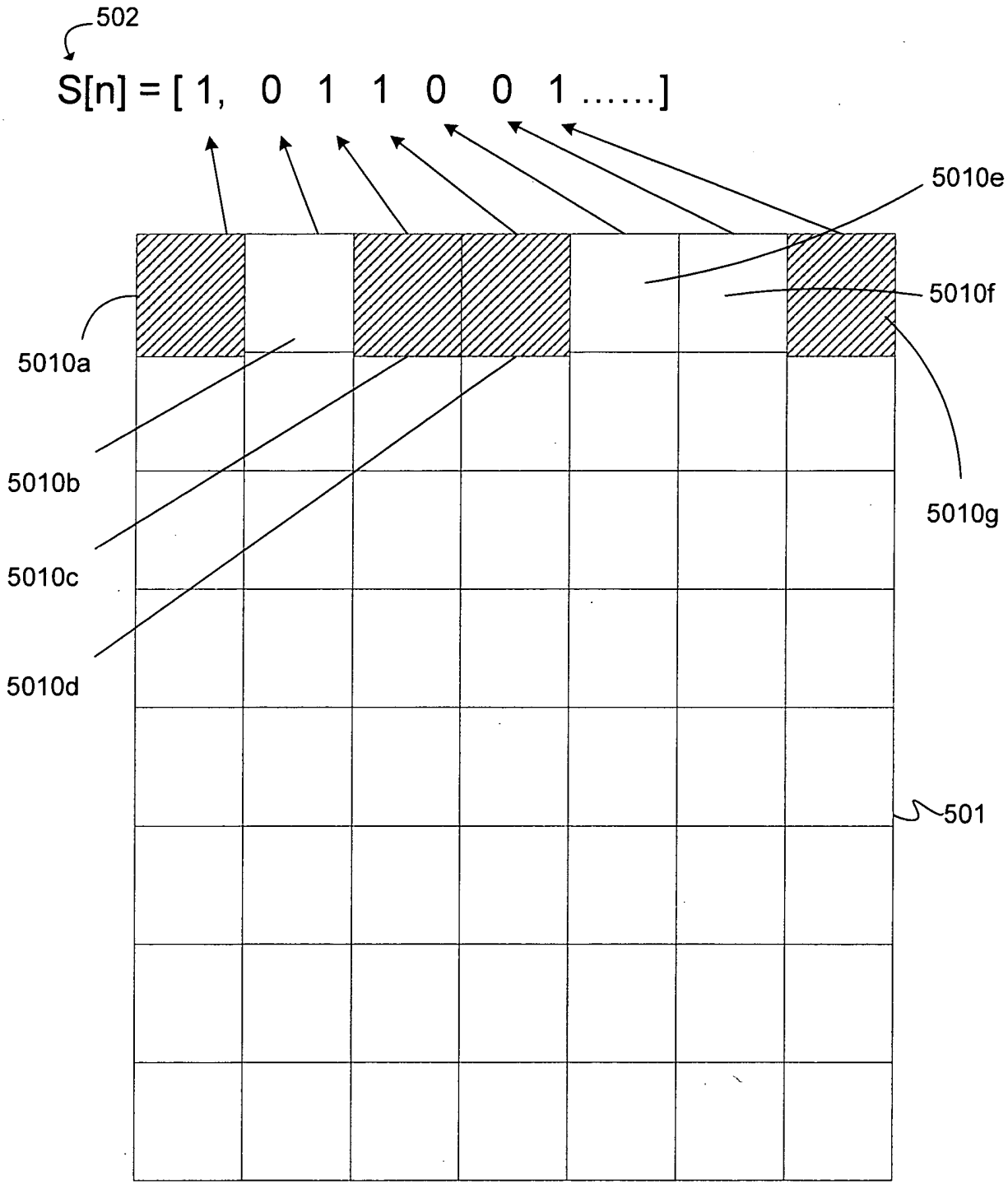
502
S[n] = [ 1, 0 1 1 0 0 1 .....]
5010e
5010f
5010a
5010b
5010c
5010d
5010g
501

FIG. 5

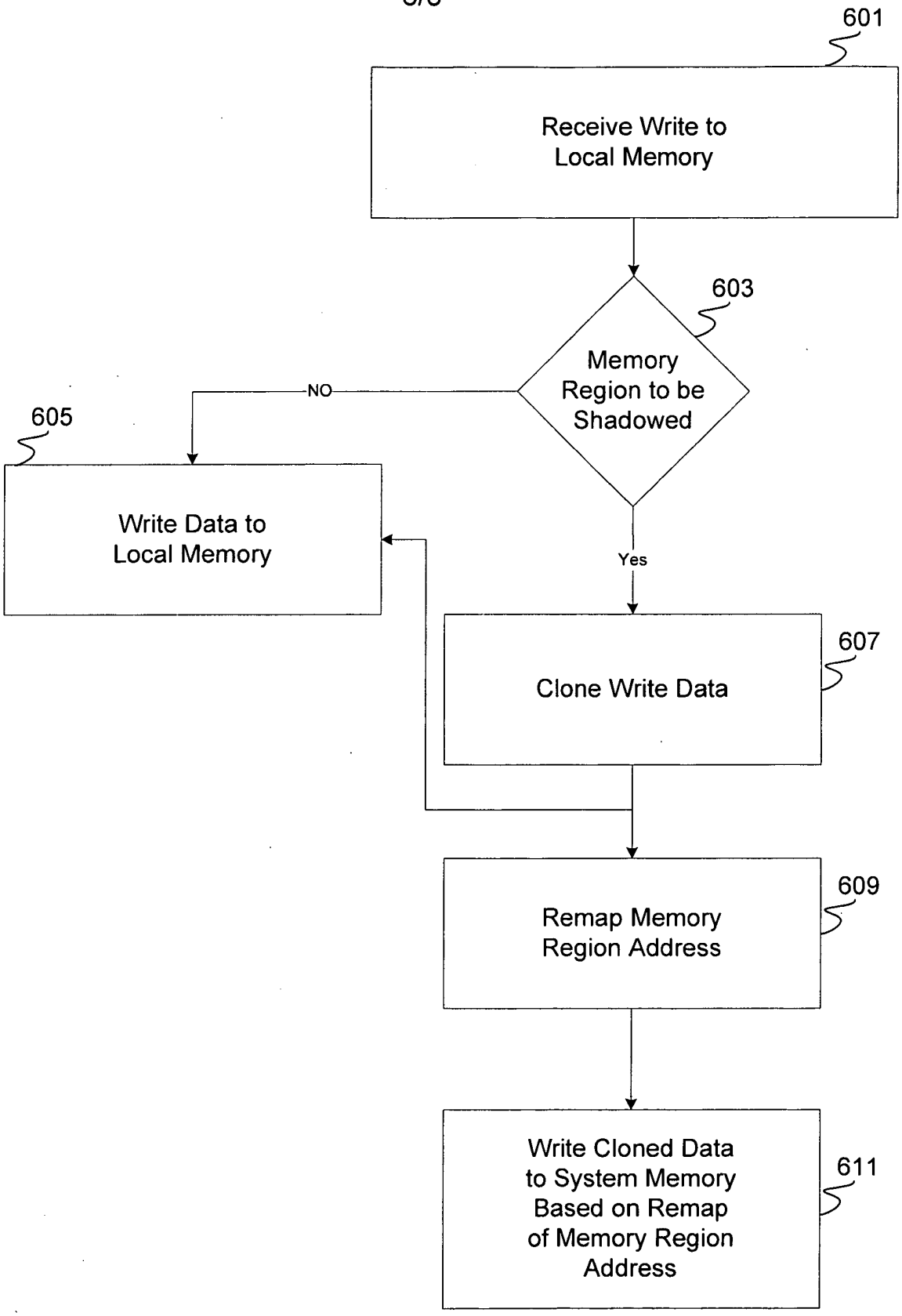
601
Receive Write to Local Memory
603
Memory Region to be Shadowed
NO
605
Write Data to Local Memory
Yes
607
Clone Write Data
609
Remap Memory Region Address
611
Write Cloned Data to System Memory Based on Remap of Memory Region Address

FIG. 6